



Linear Circuits and Systems



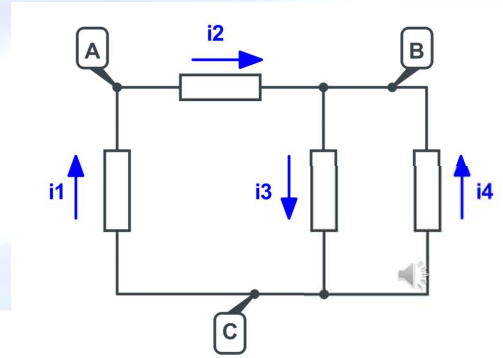
Kirchhoff's Circuit Laws

Course: Linear Circuits and Systems
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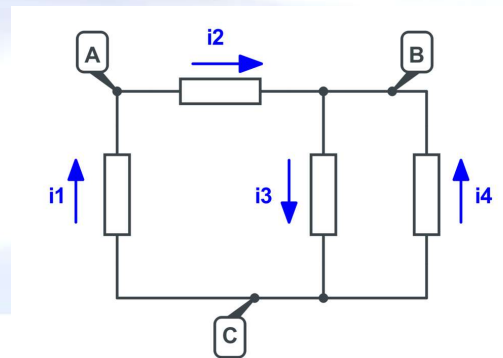
Kirchhoff's Current Law (KCL)

- The sum of all currents entering a node must equal to zero
 - *Node equation:* currents entering = currents exiting



CIRCUIT Ultimate Electronics / KCL Three Node Example
http://www.circuitlab.com/circuit/377709/kcl3

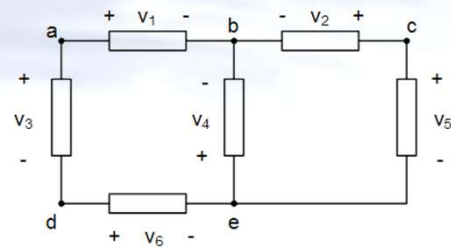
Kirchhoff's Current Law (KCL)



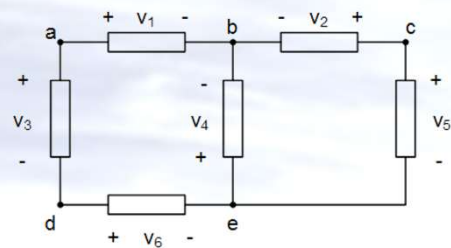
CIRCUIT Ultimate Electronics / KCL Three Node Example
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Kirchhoff's Voltage Law (KVL)

- For a loop, the voltage drops across elements in a loop must add to zero



Kirchhoff's Voltage Law (KVL)





Conclusion



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